

Programm 27. PARS-Workshop

FernUniversität in Hagen, Informatik-Zentrum, Raum F09 Erdgeschoss

Donnerstag, 4. Mai 2017

10:15 - 11:45 **Tutorial: Organisation von Special Sessions oder Workshops auf Tagungen (J. Keller)**

11:45 - 13:00 **Mittagspause**

13:00 - 13:15 **Begrüßung: Dekan der Fakultät MI, Organisatoren des Workshops**

13:15 - 14:15 **Session 1: Image Processing (Session Chair: J. Keller)**

Design Space Exploration Including Approximate Computing for OpenCL-based Stereo Vision Hardware

M. Bromberger et al., KIT

An Image Processing Operator Language for Design and Synthesis of Smart Camera Architectures

C. Hartmann et al., U Erlangen-Nürnberg

14:15 - 15:15 **Session 2: Eingeladener Vortrag (Session Chair: W. Karl)**

Potential Impact of Future Disruptive Technologies on Multicore Computing

T. Ungerer, U Augsburg

15:15 - 16:00 **Kaffeepause**

16:00 - 17:00 **Session 3: Shared Memory (Session Chair: T. Ungerer)**

Efficient Simulation of PRAM Algorithms on Shared Memory Machines

N. Berr, RWTH Aachen

Tracing of Multi-Threaded Java Applications in Score-P Using JVMTI and User Instrumentation

J. Frenzel et al., TU Dresden

17:00 - 18:00 **Mitgliederversammlung und LG-Sitzung der FG PARS**

18:15 - 18:30 Transfer zur Abendveranstaltung

18:30 - 19:30 Spaziergang an der Hohensyburg und Kaiser-Wilhelm-Denkmal

19:30 - 23:00 **Workshop-Dinner im Restaurant Altsyburg**

23:00 - 23:15 Transfer zum Hotel

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Freitag, 5. Mai 2017

08:30 - 09:30 **Session 4: Message Passing (Session Chair: C. Trinitis)**

Design of MPI Passive Target Synchronization for a Non-Cache-Coherent Many-Core Processor

S. Christgau, B. Schnor, U Potsdam

A Distributed Hash Table using One-sided Communication in MPI

P. Sobe, T. Graupner, F. Hennig, HTW Dresden

09:30 - 10:10 **Session 5: Neue Forschungsprojekte (Session Chair: K.-D. Reinartz)**

ENVELOPE – Effizienz und Zuverlässigkeit: Selbstorganisation in HPC-Systemen

W. Karl, KIT

FORMUS³IC - Multi-Core Safe and Software-intensive Systems Improvement Community

U. Margull, TH Ingolstadt

10:10 - 10:55 **Kaffeepause**

10:55 - 12:25 **Session 6: Scheduling und Vorhersage (Session Chair: P. Sobe)**

Minimizing Energy-Cost in Task-Graph Execution on Parallel Platforms

R. Gerhards, J. Keller, FeU Hagen

Predicting Efficient Execution with Source Code Analysis in A Heterogeneous Environment

M. Helwig, T. Becker, KIT

Cachepartitionierung im Kontext von Co-Scheduling

C. Trinitis, J. Weidendorfer, TUM

12:25 - 13:40 **Mittagspause**

13:40 - 15:10 **Session 7: Monitoring, Fehlertoleranz, Anwendungen (Session Chair: M. Philippsen)**

Development and implementation of a temperature monitoring system for HPC systems

M. Baumann et al., U Heidelberg

LAIK: A Library for Fault Tolerant Distribution of Global Data for Parallel Applications

J. Weidendorfer, D. Yang, C. Trinitis, TUM

Evaluating the Influence of Data Type Precision On Numerical Algorithms

M. Bromberger, M. Hoffmann, A. Hampp, KIT

15:10 - 15:15 **Schlusswort: Organisatoren des Workshops**